

Page 7, line 13, after "invention." insert:

--Figure 4A is an elevational view of a post socket.

Figure 4B is a perspective view of a post socket.

Page 7, line 14, change "Figure 4" to read --F. 5--.

Page 7, line 16, after "invention." insert:

--Figure 6A is an elevational view of a post.

Figure 6B is a top view of a brazing button hole.

IN THE CLAIMS:

Cancel claims 21 through 32.

Amend claim 1 as shown herein:

1. A method of fabricating interconnection means for a microelectronic device, the method comprising:

providing a support substrate having a first surface;
coupling a conductive sheet having a uniform thickness to the first surface of the support structure; and

selectively removing portions of the conductive sheet thereby producing a plurality of substantially [co-planar] elongated posts[,] protruding parallel to another from the first surface of the support structure, each post having a base surface and a top surface, wherein each top surface is disposed on the substrate, the top surfaces remote from the substrate and substantially coplanar with respect to one another.